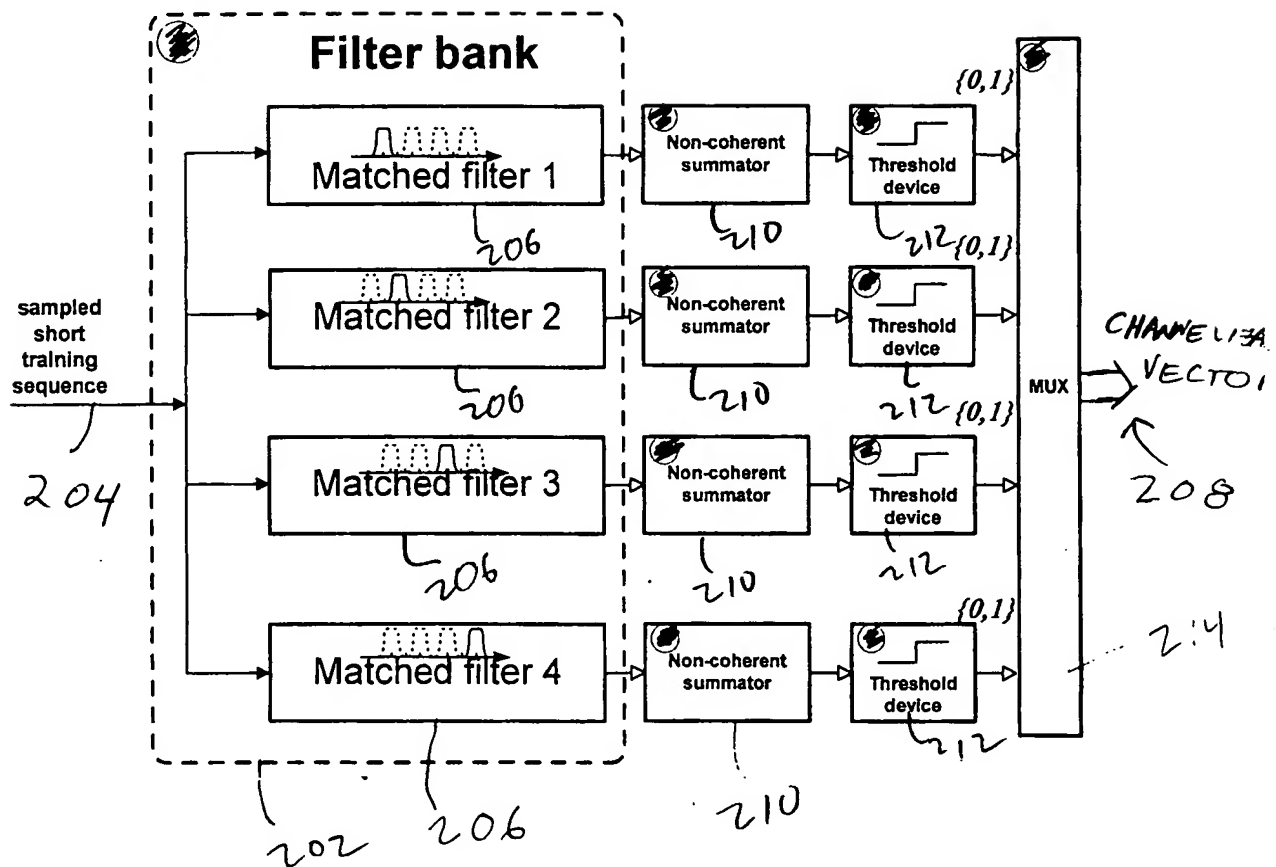


100

HIGH THROUGHPUT OFDM
RECEIVER

FIG. 1



SHORT TRAINING SYMBOL
PROCESSING CIRCUIT

FIG. 2

200

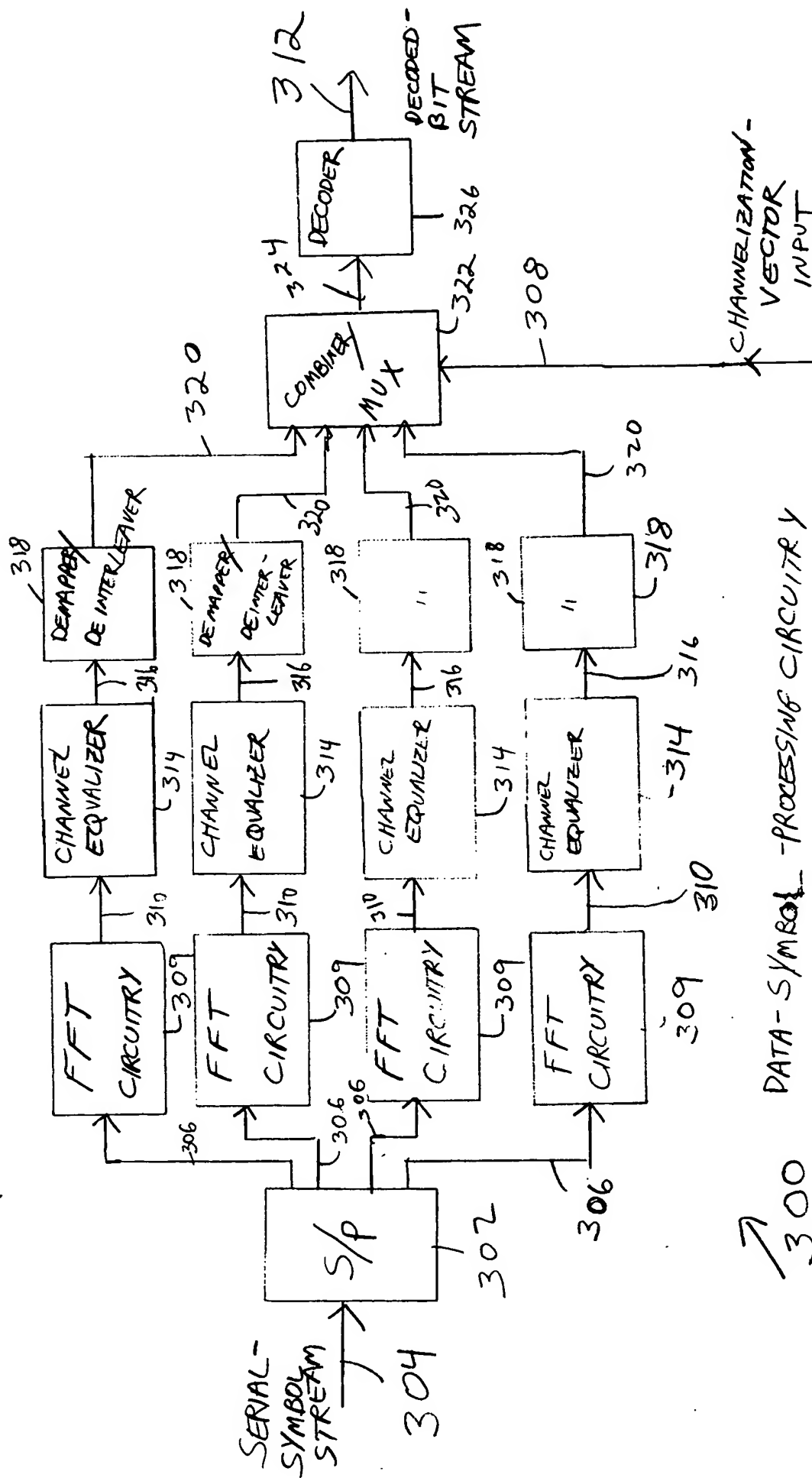
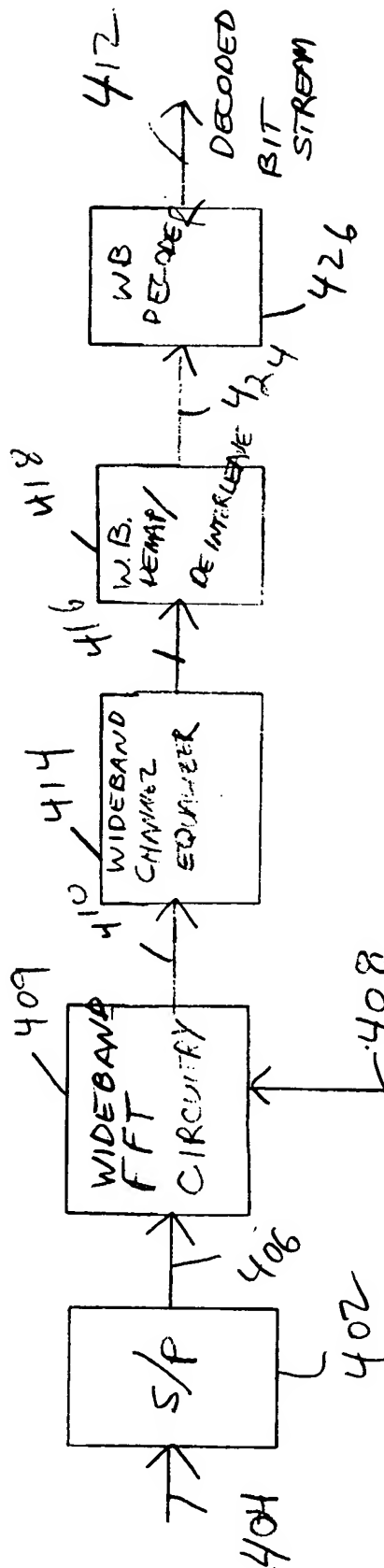


FIG. 3

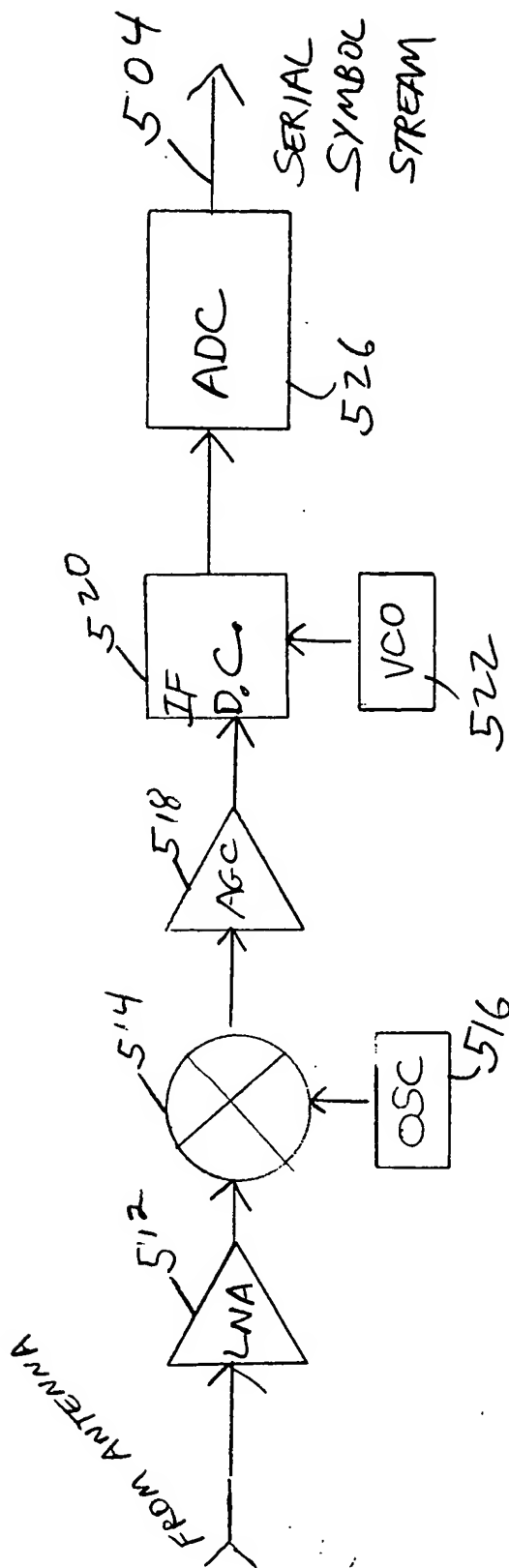
400



SYMBOL PROCESSING CIRCUITRY

FIG. 4

884A53051



500 RF RECEIVER CIRCUITRY

FIG-5

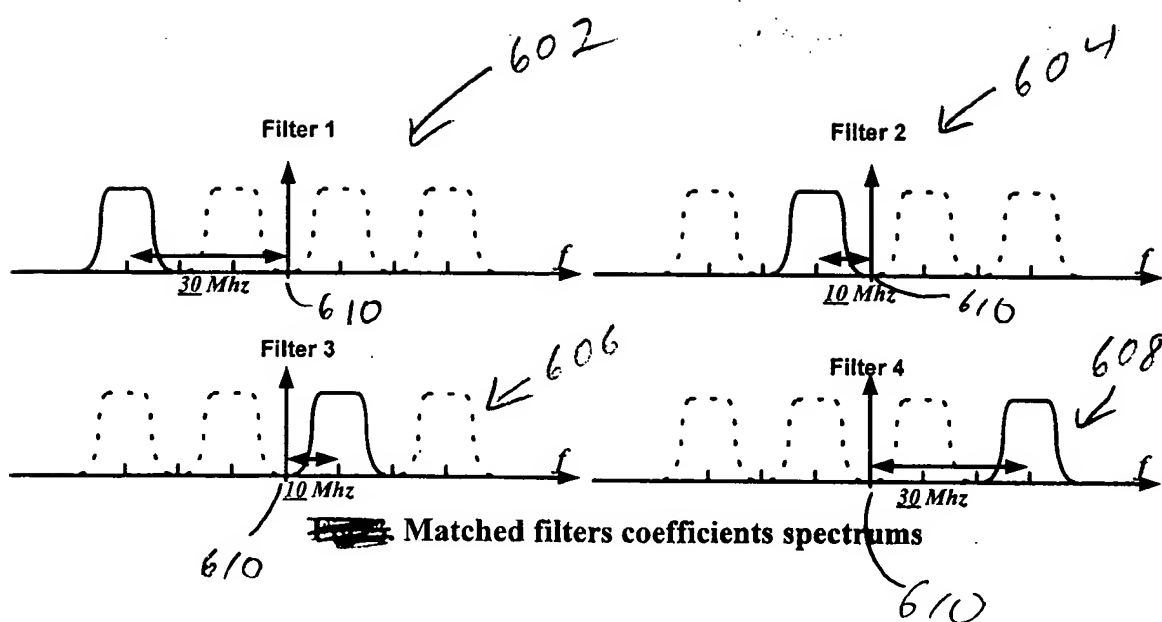
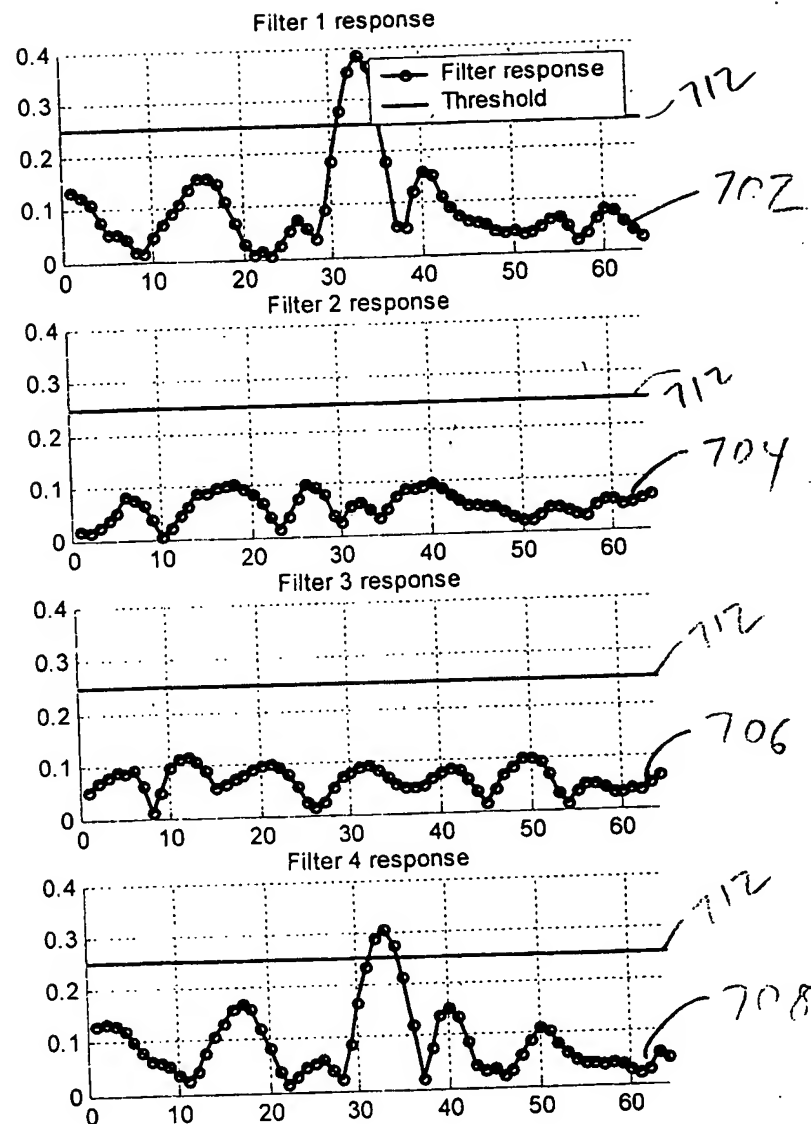


FIG 6



Channel recognition illustration for '1001' channel configuration

FIG. 7

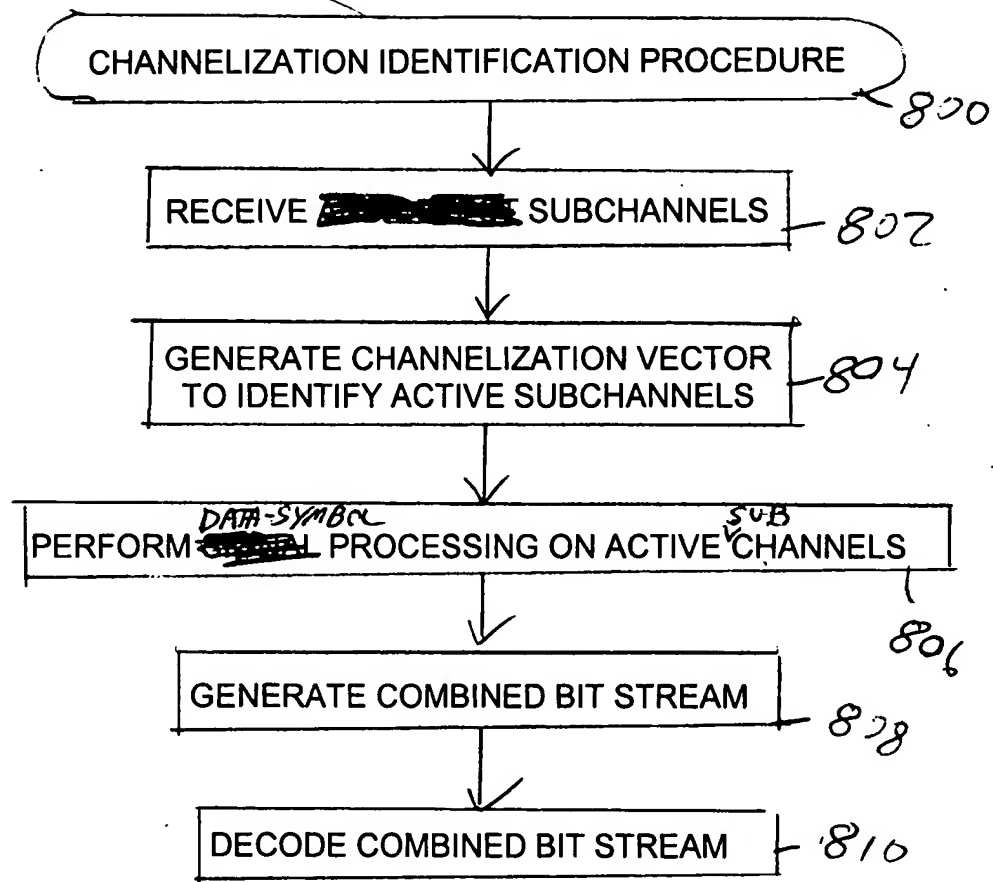


FIG. 8